

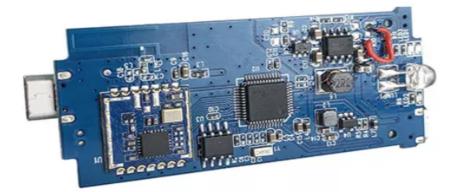
# WiFi VGA Camera Module DJU-W01

# **Product Specification**

Release version:1.0 WiFi Camera Module

# **Photos**





## 1. WIFI Camera Specification:

SPECIFICATION				
Resolution	VGA(640*480)			
Gray scale	Color			
Frame rate	30fps			
Power	USB connector (battery for option as well)			
Light Indicator	Controlled by MCU, the setting way can be met by your requirement			
Data save	On the smart device			
SSID name	SSID can change, manually connect to WIFI camera			
SDK	Different SDK for iPhone, Andriod and Windows			
Working mode-AP	WIFI camera will give out the hotspot for the smart device to connect			
WIFI camera size	Same as your size: 49.6*35mm			

### 2. WIFI CHIPSET INFORMATION

# 2.1 Applications

- MID
- IP Camera
- STB
- Smart TV
- E-book
- Other devices which need to be supported by wireless network

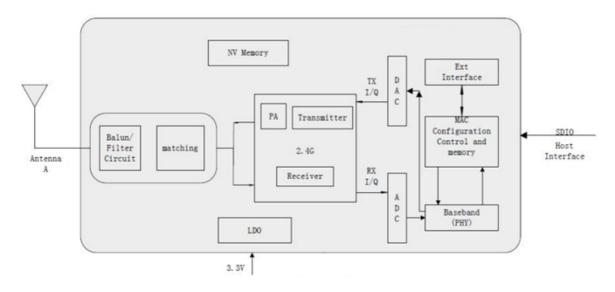
#### 2.2 Features

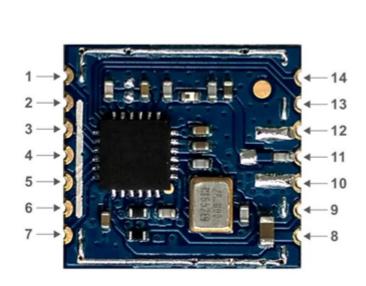
- Operating Frequencies : 2.4~2.4835GHz
- Host Interface is SDIO, complies with SDIO 1.1/2.0/3.0
- IEEE Standards : IEEE 802.11b/g/n
- Wireless data rate can reach up to 150Mbps
- External antenna optional
- Power Supply:3.3V±0.2V

#### 2.3 Key Specification

Host Interface	SDIO 1.1/ 2.0/ 3.0		
IEEE Standards	IEEE 802.11b/g/n		
Operating Frequencies	2.4~2.4835GHz,		
	802.11b: CCK, DQPSK, DBPSK		
Modulation	802.11g: 64-QAM,16-QAM, QPSK, BPSK		
	802.11n: 64-QAM, 16-QAM, QPSK, BPSK		
Working Mode	Infrastructure, Ad-Hoc		
	802.11b: 1, 2 ,5.5,11Mbps,		
Wireless Data Rate	802.11g: 6,9,12,18,24,36,48,54Mbps,		
	802.11n: MCS0~7, HT20 reach up to72.2Mbps, HT40 reach up to150Mbps		
Rx Sensitivity	-95dBm (Min)		
TX Power	19.5dBm (Max)		
Antenna Type	Connect to the external antenna through the half hole		
Dimension(L*W*H)	13x 13.5x1.5mm (WxLxH) Tolerance:+/-0.15mm		
Clock Source	26MHz		
Working Temperature	$-10^{\circ}$ C to $+50^{\circ}$ C		
Storage Temperature	-40° C to +70° C		

### 2.4 Block Diagram



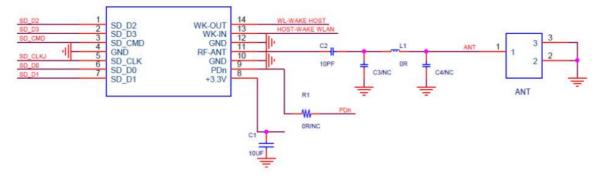


2.5 Pin Connector Descriptions:

#### PIN Function Type Description SD 2 SDIO data 2 1 I/O 2 SDIO data 3/ GSPI chip select SD 3 I/O; I SDIO command/ GSPI data input 3 SD\_CMD I/O; I 4 GND Ground G 5 SD CLK SDIO clock/ GSPI clock input I; I 6 SD D0 SDIO data 0/ GSPI data output I/O; O 7 SDIO data 1 SD D1 I/O 8 +3.3V 3.3V power supply Р 9 PDn Power down (active low) P 10 GND G Ground 11 ANT\_RF WLAN RF pad I/O 12 GND G Ground Wake/Suspend input control 13 WK IN /NC Ι 14 WK OUT 0 Wake/Suspend output control / NC

#### 7

#### 2.6 Schematic



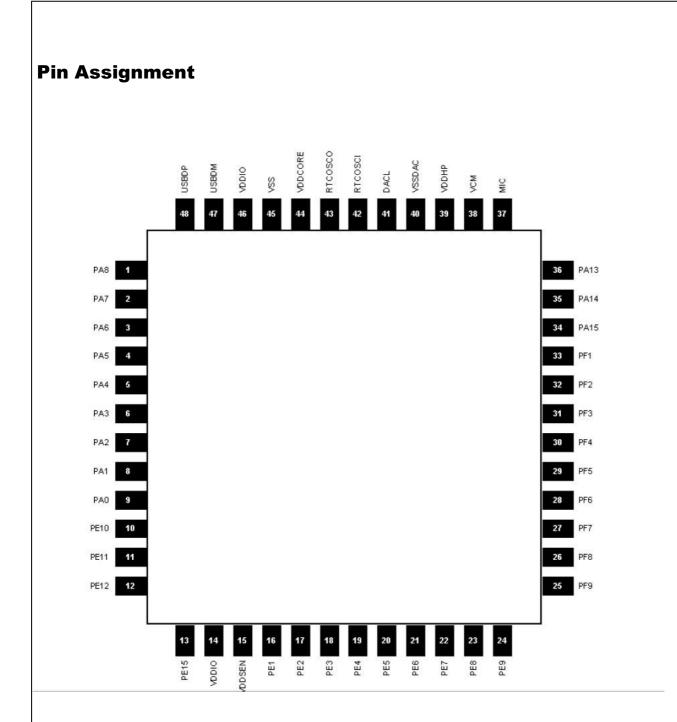
#### **3 MCU INFORMATION**

DJU-W01 uses 32bit RISC microcontroller. The project is designed to provide VGA/720P JPEG CODEC applications with cost-effective, low-power, and high-performance microcontroller solution in a small die size. By providing a complete set of common system peripherals, DJU-W01 minimizes overall system costs and eliminates the need to configure additional components. It integrates advanced digital and analog peripherals to multimedia player applications.

#### Features

- High performance 32bit CPU, Maximum 120MHz operating frequency
- 8K Bytes I-Cache and 8K Bytes D-Cache
- Embody 8MByte SDRAM
- JPEG encode & decode up to 60fps at VGA,30pfs at 720p
- Support coms sensor 8bit data interface; support and YcbCr422
- Support motion detection; VDE adjust;
- LCD driver interface; Support 8bit serial RGB LCD screen and 8bit CPU EMI LCD screen;
- Display process unit; Post-scaler supports any scale up or down; OSD1/OSD2/OSD3/Video layers; OSD1/2/3 supports 256 colors;
- Two SD Host controller
- Two SPI
- Two UART
- I2C
- Four timers
- Watch dog
- USB2.0 HS/FS Device and Host
- Multiple power LDOs
- Multiple PLL for user
- SARADC for general purpose, such as ADKEY, Battery detect;
- Mono MIC with AGC, Record
- Build in high performance audio DAC with Class AB output
- Support two oscillator at the same time, 32k and 12M
- Build in 2M RSOSC
- Support real time clock

www.dju-tech.com



#### **Pin Description**

Pin No.	Name	Туре	Function	
1	PA8	VO	IIC SDA G2 UARTO RX G1	
2	PA7	I/O	ADC3 SPI1_DI G0	

#### **DJU-TECH**

Pin No.	Name	Туре	Function	
			SD0_CMD	
			UART0_TX G2	
		1 100	ADC4	
3	PA6	I/O	SPI1_CLK G0	
			SD0_CLK	
4	PA5	I/O	SD0_DAT3	
-	244	110	SPI1_DO G0/DI_2w G0	
5	PA4	I/O	SD0_DAT0	
			UARTO_RX G2	
6	PA3	I/O	SD0_DAT1 PINT1	
7	PA2	I/O	SD0_DAT2	
1	1772	10	ADC5	
8	PA1	I/O	CSI_MCLK G0	
0	101		BTUART_RX G0	
			ADC6	
9	PA0	I/O	BTUART_TX G0	
			T3PWM G0	
10	PE10	1/0	I2C_SCL	
10	PEIU	10	TOPWM	
			ADC7	
11	PE11	I/O	IR G2	
			PINT5 G0	
			T1PWM/T2CAP/T2INC	
12	PE12	I/O	ADC8	
			T2PWM	
13	PE15	I/O	I2C_SDA G1	
14	VDDIO	PWR	BTUART_TX G1 VDDIO 3.3V LDO output	
15	VDDSEN	PWR	Sensor 3.0V LDO output	
		Fins	CSI_D0	
16	PE1	I/O	CSI_D2	
17	PE2	I/O	CSI_D1	
			CSI_D2	
18	PE3	I/O	CSI_D3	
19	PE4	I/O	CSI_D3	
19		1/0	CSI_D0	
20	PE5	I/O	CSI_D4	
21	PE6	1/0	CSI_D5	
			CSI_PCLK	
22	PE7	I/O	CSI_D6	
			CSI_D5	
23	PE8	I/O	CSI_HSYNC CSI_D6	
			CSI_PCLK	
24	PE9	I/O	CSI_POLK CSI_MCLK G1	
			LCDD4	
			LCDD7	
25	PF9	I/O	CSI_D7	
			SPI1_CLK G1	
			LCDD3	
26	PF8	I/O	LCDD6	
20	FFO	10	CSI_HSYNC	
			SPI1_DO G1/DI_2w G1	
			LCDD2	
27	PF7	I/O	LCDD5	
			CSI_VSYNC	
			SPI_PING_DATO G0	

Pin No.	Name	Туре	Function	
			LCDD4	
			SPI_PING_CLK G0	
			SD1_DAT2	
			LCDD0 LCDD3	
29	PF5	I/O	SPI_PING_CS G0	
			SD1_DAT3	
			LCDDE	
30	PF4	1/0	LCDD2	
30	PF4	10	SPI_PING_DATO G1	
			SD1_CMD	
			LCDHSYNC/LCDRS	
31	PF3	I/O	LED2/LCDD1	
			SPI_PING_CLK G1 SD1_CLK	
			LCDVSYNC/LCDCS	
			LCDD0	
32	PF2	I/O	SPI_PING_CS G1	
			SD1_DAT0	
			LCDCLK/LCDWR	
			LCDVSYNC/LCDCS	
33	PF1	VO	SPI_PING_DATI	
			SD1_DAT1 PINT2	
			LCDHSYNC/LCDRS	
34	PA15	I/O	SPI0_D1 G0/SPI0_DI G0	
05	244		SPI0 D0/SPI0 DO/DI	
35	PA14	I/O	I2C_SDA G0	
36	PA13	I/O	SPI0_CLK	
37	MICI	AL	MIC input	
38	VCM	AO	VCM output	
39	VDDHP	PWR	Header phone POWER	
00				
40	VSSADC	GND	Analog GND	
	VSSADC DACL	GND AO	Analog GND DACL Output	
40				
40 41	DACL	AO	DACL Output	
40 41 42	DACL IRTCOSCI	AO AI	DACL Output 32K OSC input	
40 41 42 43 44 45	DACL IRTCOSCI IRTCOSCO	AO AI AO	DACL Output 32K OSC input 32K OSC output	
40 41 42 43 44	DACL IRTCOSCI IRTCOSCO VDDCORE	AO AI AO PWR	DACL Output 32K OSC input 32K OSC output VDDCORE LDO output	
40 41 42 43 44 45	DACL IRTCOSCI IRTCOSCO VDDCORE VSS	AO AI AO PWR GND	DACL Output 32K OSC input 32K OSC output VDDCORE LDO output GND	

#### Note: PIN46 is the same as PIN52, PIN46 can be floating.

#### 4. USB Camera

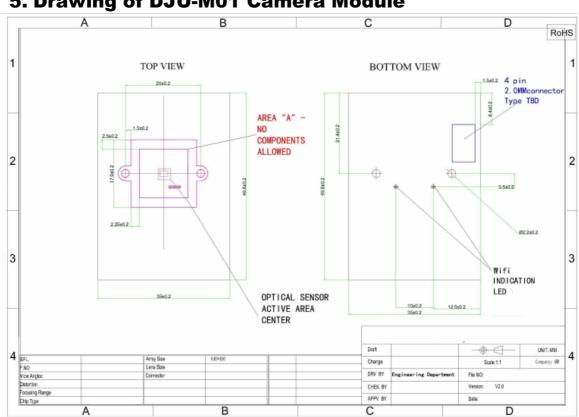
4.1 Image sensor GC0308 General Description

The GC0308 features 640V x 480H resolution with1/6.5-inch optical format, and 4-transistor pixel structure for high image quality and low noise variations.

It delivers superior image quality by powerful on-chip design of a 10-bit ADC, and embedded image signal processor

### 4.2 Image Sensor GC0308 General

- ◆ Standard optical format of 1/6.5 inch
- ♦ Various output formats: YCbCr4:2:2, RGB565, Raw Bayer
- Single power supply requirement (2.8v)
- Windowing support
- Horizontal /Vertical mirror
- Image processing module
- Package: CSP



#### 5. Drawing of DJU-M01 Camera Module

# 6. Environmental and Reliability Specification

NO	Test name	Condition	Sample size	Judgement
1	High Temperatur e storage	80°C+/-2°C 24H	5pcs	
2	Low Temperatur e storage	-40°C+/-2°C 48H	5pcs	
3	Humidity storage	60°C, 95%[RH] 72H	5pcs	1, no image change before and after
4	Thermal shock	-40°C (0.5H)~80°C (0.5H)/cycle	5pcs	2, no transformation and broken mechanically
5	Vibration test	30Hz,0.38mm&55 Hz,0.19mm, XYZ direction,0.5H/dire ct ion,	10pcs	3, no focus changing of lens 4, vision inspection OK
6	Drop test	1m/one direction. 1time/direction, total 6 direction	10pcs	