# Dju-Tech 

# WiFi Camera Module DJU-W02 

## Product Specification

Release version:1.0
WiFi Camera Module

## Photos



## 1. WIFI Camera Specification:

| SPECIFICATION |  |
| :--- | :--- |
| Resolution | VGA(640*480) |
| Gray scale | Color |
| Frame rate | 3Ofps |
| Power | USB connector (battery for option as well) |
| Light Indicator | Controlled by MCU, the setting way can be met by your requirement |
| Data save | On the smart device |
| SSID name | SSID can change, manually connect to WIFI camera |
| SDK | Different SDK for iPhone, Andriod and Windows |
| Working mode-AP | WIFI camera will give out the hotspot for the smart device to connect |
| WIFI camera size | Same as your size: $49.6 * 35 \mathrm{~mm}$ |

## 2. WIFI CHIPSET INFORMATION

### 2.1 Applications

- MID
- IP Camera
- STB
- Smart TV
- E-book
- Other devices which need to be supported by wireless network


### 2.2 Features

- Operating Frequencies : $2.4 \sim 2.4835 \mathrm{GHz}$
- Host Interface is SDIO, complies with SDIO 1.1/2.0/3.0
- IEEE Standards : IEEE $802.11 \mathrm{~b} / \mathrm{g} / \mathrm{n}$
- Wireless data rate can reach up to 150 Mbps
- External antenna optional
- Power Supply:3.3V $\pm 0.2 \mathrm{~V}$


### 2.3 Key Specification

| Host Interface | SDIO $1.1 / 2.0 / 3.0$ |
| :--- | :--- |
| IEEE Standards | IEEE $802.11 \mathrm{~b} / \mathrm{g} / \mathrm{n}$ |
| Operating Frequencies | $2.4 \sim 2.4835 \mathrm{GHz}$, |
|  | $802.11 \mathrm{~b}: \mathrm{CCK}, \mathrm{DQPSK}, \mathrm{DBPSK}$ |
| Modulation | $802.11 \mathrm{~g}: 64-\mathrm{QAM}, 16-\mathrm{QAM}, \mathrm{QPSK}, \mathrm{BPSK}$ |
|  | $802.11 \mathrm{n}: 64-\mathrm{QAM}, 16-\mathrm{QAM}, \mathrm{QPSK}, \mathrm{BPSK}$ |
| Working Mode | Infrastructure, Ad-Hoc |
|  | $802.11 \mathrm{~b}: 1,2,5.5,11 \mathrm{Mbps}$, |
| Wireless Data Rate | $802.11 \mathrm{~g}: 6,9,12,18,24,36,48,54 \mathrm{Mbps}$, |
|  | $802.11 \mathrm{n}: \mathrm{MCS} 0 \sim 7, \mathrm{HT} 20$ reach up to72.2Mbps, HT40 reach up to150Mbps |
| Rx Sensitivity | $-95 \mathrm{dBm}(\mathrm{Min})$ |
| TX Power | $19.5 \mathrm{dBm}(\mathrm{Max})$ |
| Antenna Type | Connect to the external antenna through the half hole |
| Dimension(L*W* $\left.{ }^{*} \mathrm{H}\right)$ | $13 \mathrm{x} 13.5 \mathrm{x} 1.5 \mathrm{~mm}(\mathrm{WxLxH})$ Tolerance: $+/-0.15 \mathrm{~mm}$ |
| Clock Source | 26 MHz |
| Working Temperature | $-10^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}$ |
| Storage Temperature | $-40^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |

### 2.4 Block Diagram



### 2.5 Pin Connector Descriptions:



| PIN | Function | Type | Description |
| :--- | :--- | :--- | :--- |
| 1 | SD_2 | I/O | SDIO data 2 |
| 2 | SD_3 | I/O $;$ I | SDIO data 3/ GSPI chip select |
| 3 | SD_CMD | I/O $;$ I | SDIO command/ GSPI data input |
| 4 | GND | G | Ground |
| 5 | SD_CLK | I; I | SDIO clock/ GSPI clock input |
| 6 | SD_D0 | I/O $;$ O | SDIO data 0/ GSPI data output |
| 7 | SD_D1 | I/O | SDIO data 1 |
| 8 | +3.3V | P | $3.3 V$ power supply |
| 9 | PDn | P | Power down (active low) |
| 10 | GND | G | Ground |
| 11 | ANT_RF | I/O | WLAN RF pad |
| 12 | GND | G | Ground |
| 13 | WK_IN | I | Wake/Suspend input control |
| 14 | WK_OUT | O | Wake/Suspend output control |

## DJU-TECH

### 2.6 Schematic



## 3 MCU INFORMATION

DJU-W02 uses 32bit RISC microcontroller. The project is designed to provide VGA/720P JPEG CODEC applications with cost-effective, low-power, and high-performance microcontroller solution in a small die size. By providing a complete set of common system peripherals, DJU-W01 minimizes overall system costs and eliminates the need to configure additional components. It integrates advanced digital and analog peripherals to multimedia player applications.

## Features

- High performance 32bit CPU, Maximum 120 MHz operating frequency
- 8K Bytes I-Cache and 8 K Bytes D-Cache
- Embody 8MByte SDRAM
- JPEG encode \& decode up to 60fps at VGA,30pfs at 720p
- Support coms sensor 8bit data interface; support and YcbCr422
- Support motion detection; VDE adjust;
- LCD driver interface; Support 8bit serial RGB LCD screen and 8bit CPU EMI LCD screen;
- Display process unit; Post-scaler supports any scale up or down; OSD1/OSD2/OSD3/Video layers; OSD1/2/3 supports 256 colors;
- Two SD Host controller
- Two SPI
- Two UART
- I2C
- Four timers
- Watch dog
- USB2.0 HS/FS Device and Host
- Multiple power LDOs
- Multiple PLL for user
- SARADC for general purpose, such as ADKEY,Battery detect;
- Mono MIC with AGC,Record
- Build in high performance audio DAC with Class AB output
- Support two oscillator at the same time, 32 k and 12 M
- Build in 2M RSOSC
- Support real time clock


## Pin Assignment



## Pin Description

| Pin No. | Name | Type | Function |
| :--- | :--- | :--- | :--- |
| 1 | PA8 | I/O | IIC SDA G2 <br> UART0 RX G1 |
| 2 | PA7 | I/O | ADC3 <br> SPI1_DI G0 |


| Pin No. | Name | Type | Function |
| :---: | :---: | :---: | :---: |
|  |  |  | SDO_CMD UART0 TX G2 |
| 3 | PA6 | I/O | ADC4 <br> SPI1_CLK G0 <br> SDO_CLK |
| 4 | PA5 | I/O | SDO_DAT3 |
| 5 | PA4 | I/O | $\begin{aligned} & \text { SPI1_DO GO/DI_2w G0 } \\ & \text { SDO_DATO } \\ & \text { UARTO_RX G2 } \end{aligned}$ |
| 6 | PA3 | I/O | SDO_DAT1 PINT1 |
| 7 | PA2 | I/O | SDO_DAT2 |
| 8 | PA1 | I/O | ADC5 <br> CSI_MCLK GO <br> BTUART_RX GO |
| 9 | PAO | I/O | ADC6 BTUART_TX G0 T3PWM G0 |
| 10 | PE10 | I/O | I2C SCL TOPWM |
| 11 | PE11 | I/O | ADC7 <br> IR G2 <br> PINT5 G0 <br> T1PWM/T2CAP/T2INC |
| 12 | PE12 | I/O | ADC8 T2PWM |
| 13 | PE15 | I/O | $\begin{aligned} & \text { I2C_SDA G1 } \\ & \text { BTUART_TX G1 } \end{aligned}$ |
| 14 | VDDIO | PWR | VDDIO 3.3V LDO output |
| 15 | VDDSEN | PWR | Sensor 3.0V LDO output |
| 16 | PE1 | I/O | $\begin{aligned} & \text { CSI_D0 } \\ & \text { CSI_D2 } \end{aligned}$ |
| 17 | PE2 | I/O | CSI_D1 |
| 18 | PE3 | I/O | $\begin{aligned} & \text { CSI_D2 } \\ & \text { CSI_D3 } \end{aligned}$ |
| 19 | PE4 | I/O | $\begin{aligned} & \text { CSI_D3 } \\ & \text { CSI_D0 } \end{aligned}$ |
| 20 | PE5 | I/O | CSI_D4 |
| 21 | PE6 | I/O | $\begin{aligned} & \text { CSI_D5 } \\ & \text { CSI_PCLK } \end{aligned}$ |
| 22 | PE7 | I/O | $\begin{aligned} & \text { CSI_D6 } \\ & \text { CSI_D5 } \end{aligned}$ |
| 23 | PE8 | I/O | $\begin{aligned} & \text { CSI_HSYNC } \\ & \text { CSI_D6 } \end{aligned}$ |
| 24 | PE9 | I/O | CSI_PCLK CSI_MCLK G1 |
| 25 | PF9 | I/O | LCDD4 <br> LCDD7 <br> CSI_D7 <br> SPI1 CLK G1 |
| 26 | PF8 | I/O | LCDD3 <br> LCDD6 <br> CSI_HSYNC <br> SPI1_DO G1/DI 2w G1 |
| 27 | PF7 | I/O | $\begin{aligned} & \text { LCDD2 } \\ & \text { LCDD5 } \\ & \text { CSI_VSYNC } \\ & \text { SPI_PING_DATO G0 } \end{aligned}$ |
| 28 | PF6 | I/O | LCDD1 |


| Pin No.l | Name | Type | Function |
| :---: | :---: | :---: | :---: |
|  |  |  | LCDD4 SPI_PING_CLK GO SD1_DAT2 |
| 29 | PF5 | I/O | ```LCDDO LCDD3 SPI_PING CS GO SD1 DAT3``` |
| 30 | PF4 | I/O | LCDDE <br> LCDD2 <br> SPI_PING_DATO G1 <br> SD1_CMD |
| 31 | PF3 | I/O | LCDHSYNC/LCDRS LED2/LCDD1 SPI_PING_CLK G1 SD1_CLK |
| 32 | PF2 | I/O | LCDVSYNC/LCDCS <br> LCDDO <br> SPI_PING_CS G1 <br> SD1_DATO |
| 33 | PF1 | I/O | LCDCLK/LCDWR <br> LCDVSYNC/LCDCS <br> SPI_PING_DATI <br> SD1_DAT1 <br> PINT2 |
| 34 | PA15 | I/O | LCDHSYNC/LCDRS SPIO_D1 GO/SPIO_DI GO |
| 35 | PA14 | I/O | $\begin{aligned} & \text { SPIO_DO/SPIO_DO/DI } \\ & \text { I2C_SDA GO } \end{aligned}$ |
| 36 | PA13 | I/O | SPIO_CLK |
| 37 | MICI | AI | MIC input |
| 38 | VCM | AO | VCM output |
| 39 | VDDHP | PWR | Header phone POWER |
| 40 | VSSADC | GND | Analog GND |
| 41 | DACL | AO | DACL Output |
| 42 | IRTCOSCI | Al | 32K OSC input |
| 43 | IRTCOSCO | AO | 32K OSC output |
| 44 | VDDCORE | PWR | VDDCORE LDO output |
| 45 | VSS | GND | GND |
| 6 | VDDIO | PWR | USB VDD |
| 47 | USBDM | AIO | USB2.0 DM |
| 48 | USBDP | AIO | USB2.0 DP |

Note: PIN46 is the same as PIN52, PIN46 can be floating.

## 4. USB Camera

4.1 Image sensor GC0308 General Description

The GC0308 features $640 \mathrm{~V} \times 480 \mathrm{H}$ resolution with1/6.5-inch optical format, and 4-transistor pixel structure for high image quality and low noise variations.

It delivers superior image quality by powerful on-chip design of a 10-bit ADC, and embedded image signal processor

### 4.2 Image Sensor GC0308 General

- Standard optical format of $1 / 6.5$ inch
- Various output formats: YCbCr4:2:2, RGB565, Raw Bayer
- Single power supply requirement (2.8v)
- Windowing support
- Horizontal /Vertical mirror
- Image processing module
- Package: CSP

5. Drawing of DJU-M02 Camera Module


## 6. Environmental and Reliability Specification

| NO | Test name | Condition | Sample size | Comments |
| :---: | :---: | :---: | :---: | :---: |
| 1 | $\begin{array}{c}\text { High } \\ \text { Temperatur } \\ \text { e storage }\end{array}$ | $80^{\circ} \mathrm{C}+/-2^{\circ} \mathrm{C} 24 \mathrm{H}$ | 5 pcs |  |
| 2 | $\begin{array}{c}\text { Low } \\ \text { Temperatur } \\ \text { e storage }\end{array}$ | $-40^{\circ} \mathrm{C}+/-2^{\circ} \mathrm{C} 48 \mathrm{H}$ | 5 pcs |  |
| 3 | $\begin{array}{c}\text { Humidity } \\ \text { storage }\end{array}$ | $60^{\circ} \mathrm{C}, 95 \%[\mathrm{RH}] 72 \mathrm{H}$ | 5 pcs | 1 , no image change before and |
| after |  |  |  |  |$)$

